

730V Super-junction Power MOSFET

Description

730V Super-junction Power MOSFET

Super-junction power MOSFET is a revolutionary technology for high voltage power MOSFETs, designed according to the SJ principle. The Multi-EPI SJ MOSFET provide an extremely low switching, communication and conduction losses device with highest robustness make especially resonant switching applications more reliable, more efficient, lighter and cooler, designed by Wuxi Unigroup Microelectronics Company.

Features

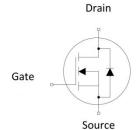
- Very low FOM RDS(on)×Qg
- 100% avalanche tested
- Easy to use/drive
- RoHS compliant

Applications

- Switch Mode Power Supply (SMPS)
- Uninterruptible Power Supply (UPS)
- Power Factor Correction (PFC)
- Charger

TO-220F







Device Marking and Package Information

Device	Package	Marking	
TPA73R400M	TO-220F	73R400M	

Key Performance Parameters

They is a summarised in an amount of the summarised in a summa					
Parameter	Value	Unit			
V _{DS} @ T _{j,max}	780	V			
R _{DS(on),max}	0.4	Ω			
$Q_{g,typ}$	20.5	nC			
I _D	11	A			
I _{D,pulse}	33	A			
E _{OSS} @ 400V	2.29	μJ			
Body Diode di _F /dt	500	A/µs			



Absolute Maximum Ratings $T_C = 25^{\circ}C$, unless otherwise noted					
Parameter			Symbol	Value	Unit
Continuous Dunin Current	T _C = 25°C		,	11	^
Continuous Drain Current	T _C = 100°C		- I _D	6.6	A
Pulsed Drain Current (note1)			I _{D,pulse}	33	А
Gate-Source Voltage			V_{GSS}	±30	V
Single Pulse Avalanche Energy		(note2)	E _{AS}	215	mJ
Repetitive Avalanche Energy (note2)			E _{AR}	0.32	mJ
Avalanche Current			I _{AR}	1.8	А
MOSFET dv/dt Ruggedness, V _{DS} = 0480V			dv/dt	50	V/ns
Power Dissipation For TO-220F			P_D	31	W
Continuous Diode Forward Current			Is	9.4	A
Diode Pulsed Current (note		(note1)	I _{S,pulse}	33	
Reverse Diode dv/dt (note3)		(note3)	dv/dt	15	V/ns
Maximum Diode Commutation Speed (note3)		(note3)	di _f /dt	500	A/µs
Operating Junction and Storage Temperature Range			T_J,T_stg	-55~+150	°C

Thermal Resistance For TO-220F			
Parameter	Symbol	Value	Unit
Thermal Resistance, Junction-to-Case	R _{thJC}	4	°C/W
Thermal Resistance, Junction-to-Ambient	R _{thJA}	80] C/VV



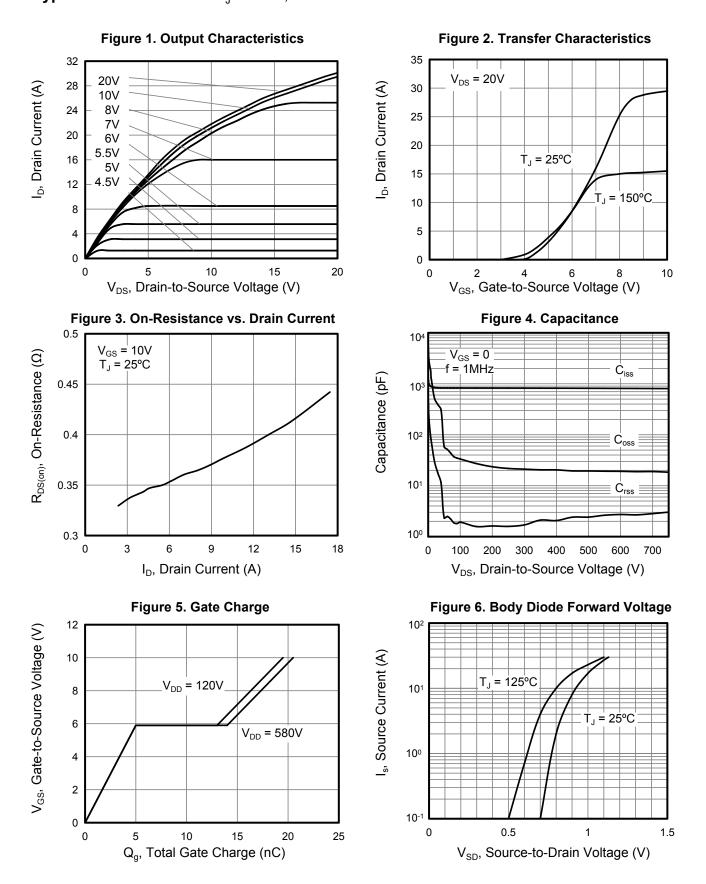
			Value			
Parameter	Symbol	Test Conditions	Min.	Тур.	Max.	Unit
Static Characteristics	•			•		
Drain-Source Breakdown Voltage	V _{(BR)DSS}	$V_{GS} = 0V, I_D = 250\mu A$	730			V
Zero Gate Voltage Drain Current					1	
Zero Gate Voltage Drain Current	I _{DSS}			100	μA	
Gate-Source Leakage Current	I _{GSS}	$V_{GS} = \pm 30V$			±100	nA
Gate-Source Threshold Voltage	$V_{GS(th)}$	$V_{DS} = V_{GS}, I_{D} = 250 \mu A$	2.5		4.5	٧
Drain-Source On-State-Resistance	R _{DS(on)}	V _{GS} = 10V, I _D = 5.5A		0.35	0.4	Ω
Gate Resistance	R_{G}	f = 1.0MHz open drain		18		Ω
Dynamic Characteristics				•		
Input Capacitance	C _{iss}	\/ - 0\/		828		pF
Output Capacitance	C _{oss}	$V_{GS} = 0V,$ $V_{DS} = 100V,$		33		
Reverse Transfer Capacitance	C _{rss}	f = 1.0MHz		1.9		
Total Gate Charge	Q_g			20.5		
Gate-Source Charge	Q_{gs}	$V_{DD} = 580V, I_{D} = 11A,$ $V_{GS} = 10V$		5		nC
Gate-Drain Charge	Q_{gd}	63		9		
Turn-on Delay Time	t _{d(on)}			22		
Turn-on Rise Time	t_{r} $V_{DD} = 400V, I_{D} = 11A,$			21		
Turn-off Delay Time	$t_{d(off)}$	$R_G = 25\Omega$		115		ns
Turn-off Fall Time	t _f		-	36		
Drain-Source Body Diode Characte	ristics					
Body Diode Forward Voltage	V _{SD}	$T_J = 25^{\circ}\text{C}, I_{SD} = 11\text{A}, V_{GS} = 0\text{V}$		0.9	1.2	V
Reverse Recovery Time	t _{rr}			314		ns
Reverse Recovery Charge	Q _{rr}	V _R = 400V, I _F = 11A, di _F /dt = 100A/μs	-	3.08		μC
Peak Reverse Recovery Current	I _{rrm}			19.6		Α

Notes

- 1. Repetitive Rating: Pulse width limited by maximum junction temperature
- 2. I_{AS} = 2.4A, V_{DD} = 50V, R_G = 25 Ω , Starting T_J = 25 $^{\circ}$ C
- 3. Identical low side and high side switch with identical $R_{\mbox{\scriptsize G}}$



Typical Characteristics $T_J = 25^{\circ}\text{C}$, unless otherwise noted





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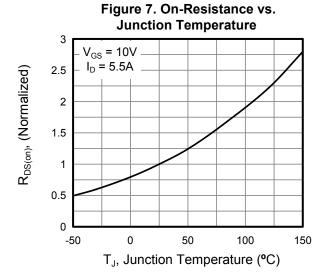


Figure 9. Transient Thermal Impedance For TO-220F

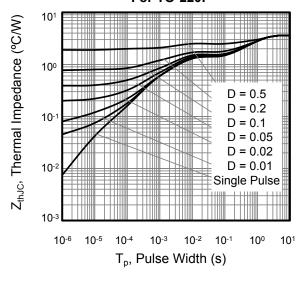


Figure 11. Typ. Coss Stored Energy

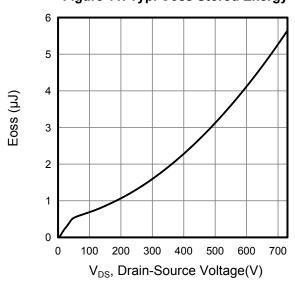


Figure 8.Breakdown voltage vs. Junction Temperature

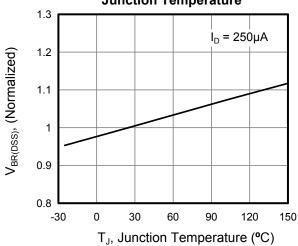


Figure 10. Safe Operation Area For TO-220F

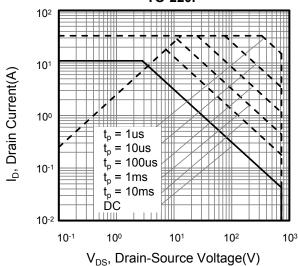


Figure A: Gate Charge Test Circuit and Waveform

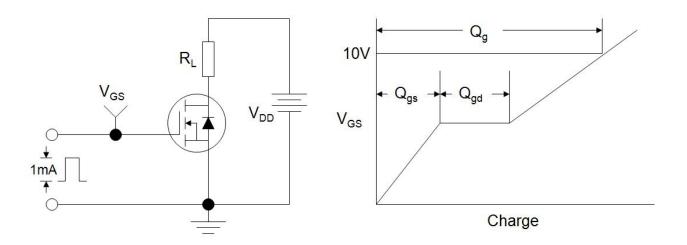


Figure B: Resistive Switching Test Circuit and Waveform

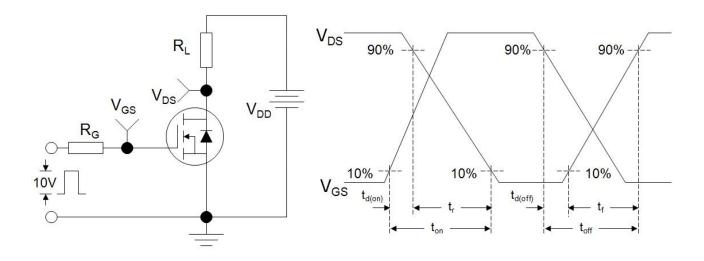
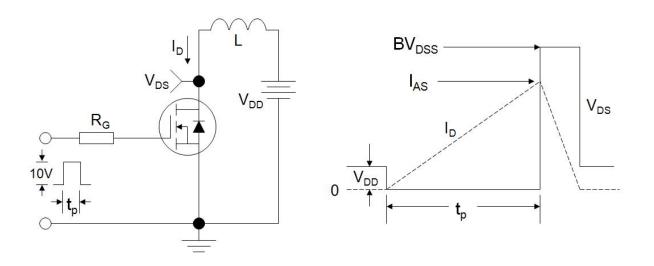
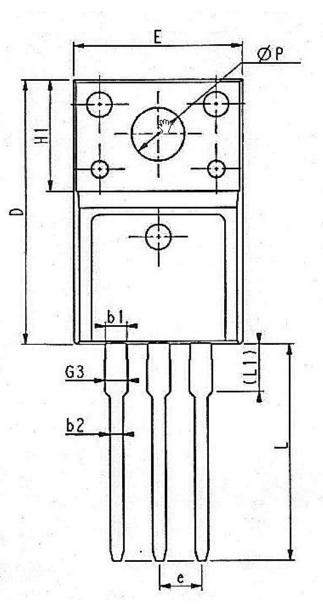


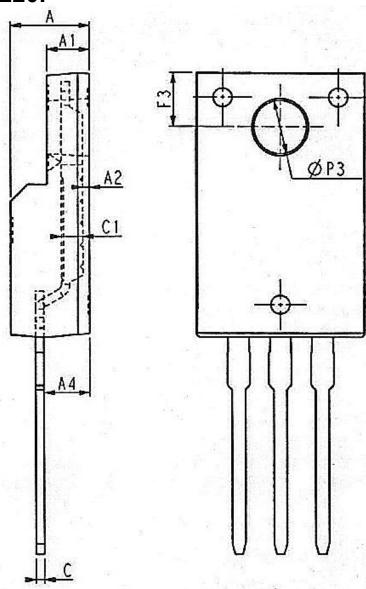
Figure C: Unclamped Inductive Switching Test Circuit and Waveform





TO-220F





Unit:mm						
Symbol	Min.	Nom	Max.			
Е	9.96	10.16	10.36			
Α	4.50	4.70	4.90			
A1	A1 2.34 A2 0.30		2.74			
A2			0.60			
A4	2.56	2.76	2.96			
С	0.40	0.50	0.65			
c1	1.20	1.30	1.35			
D	15.57	15.87	16.17			
H1	6.70REF					

Unit:mm						
Symbol	Min.	Nom	Max.			
е	2.54BSC					
L	12.68	12.98	13.28			
L1	2.88	3.03	3.18			
ФР	3.03	3.18	3.38			
ФР3	3.15	3.45	3.65			
F3	3.15	3.30	3.45			
G3	1.25	1.35	1.55			
b1	1.18	1.28	1.43			
b2	0.70	0.80	0.95			



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