
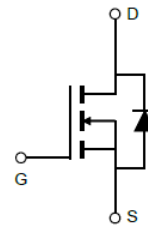
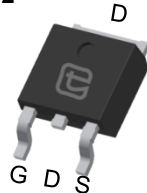


**82V N-Channel Trench MOSFET(Preliminary)**

<p>General Description</p> <ul style="list-style-type: none"> ● Trench Power technology ● Low $R_{DS(ON)}$ ● Low Gate Charge ● Optimized for fast-switching applications <p>Applications</p> <ul style="list-style-type: none"> ● Synchronous Rectification in DC/DC and AC/DC Converters ● Isolated DC/DC Converters in Telecom and Industrial 	<p>Product Summary</p> <table> <tr> <td>V_{DS}</td> <td>82V</td> </tr> <tr> <td>I_D (at $V_{GS}=10V$)</td> <td>88A</td> </tr> <tr> <td>$R_{DS(ON)}$ (at $V_{GS}=10V$)</td> <td><8.5mΩ</td> </tr> </table> <p>100% UIS Tested</p> 	V_{DS}	82V	I_D (at $V_{GS}=10V$)	88A	$R_{DS(ON)}$ (at $V_{GS}=10V$)	<8.5m Ω
V_{DS}	82V						
I_D (at $V_{GS}=10V$)	88A						
$R_{DS(ON)}$ (at $V_{GS}=10V$)	<8.5m Ω						

TO-252



Part Number	Package Type	Form	Marking
TTD88N08A	TO-252	Tube	TTD88N08A

Absolute Maximum Ratings ($T_A = 25^\circ\text{C}$ unless otherwise noted)

Parameter	Symbol	Maximum	Units
Drain-Source Voltage	V_{DS}	82	V
Gate-Source Voltage	V_{GS}	± 20	V
Continuous Drain Current ^B	I_D	$T_C = 25^\circ\text{C}$	46
		$T_C = 100^\circ\text{C}$	30
Pulsed Drain Current ^A	I_{DM}	264	A
Avalanche Current ^A	I_{AS}	43	A
Single Pulse Avalanche Energy $L = 0.3\text{mH}$ ^A	E_{AS}	277	mJ
Power Dissipation ^C	P_D	$T_C = 25^\circ\text{C}$	174
		$T_C = 100^\circ\text{C}$	87
Junction and Storage Temperature Range	T_J, T_{STG}	-55 to 175	$^\circ\text{C}$

Thermal Characteristics

Parameter	Symbol	Maximum	Units
Maximum Junction-to-Case	Steady-State $R_{\theta JC}$	0.86	$^\circ\text{C/W}$
Maximum Junction-to-Ambient	Steady-State $R_{\theta JA}$	100	



Electrical Characteristics($T_J = 25^\circ\text{C}$ unless otherwise noted)							
Symbol	Parameter	Conditions	Value			Units	
			Min	Typ	Max		
STATIC PARAMETERS							
BV_{DSS}	Drain-Source Breakdown Voltage	$I_D = 250\mu\text{A}, V_{GS} = 0\text{V}$	82	--	--	V	
I_{DSS}	Zero Gate Voltage Drain Current	$V_{DS} = 82\text{V}, V_{GS} = 0\text{V}$	$T_J = 25^\circ\text{C}$	--	--	1	μA
			$T_J = 125^\circ\text{C}$	--	--	100	
I_{GSS}	Gate-Body Leakage Current	$V_{DS} = 0\text{V}, V_{GS} = \pm 20\text{V}$	--	--	± 100	nA	
$V_{GS(th)}$	Gate Threshold Voltage	$V_{DS} = V_{GS}, I_D = 250\mu\text{A}$	2	3	4	V	
$R_{DS(on)}$	Static Drain-Source On-Resistance	$V_{GS} = 10\text{V}, I_D = 30\text{A}$	--	7.4	8.5	m Ω	
g_{FS}	Forward Transconductance	$V_{DS} = 5\text{V}, I_D = 20\text{A}$	--	37	--	S	
V_{SD}	Diode Forward Voltage	$I_S = 30\text{A}, V_{GS} = 0\text{V}$	--	--	1	V	
I_S	Maximum Body-Diode Continuous Current ^B		--	--	88	A	
DYNAMIC PARAMETERS							
C_{iss}	Input Capacitance	$V_{GS} = 0\text{V}, V_{DS} = 40\text{V}, f = 1\text{MHz}$	--	5341	--	pF	
C_{oss}	Output Capacitance		--	263	--		
C_{rss}	Reverse Transfer Capacitance		--	241	--		
R_g	Gate Resistance	$f = 1\text{MHz}$	--	1.5	--	Ω	
SWITCHING PARAMETERS							
Q_g	Total Gate Charge	$V_{GS} = 10\text{V}, V_{DS} = 40\text{V}, I_D = 20\text{A}$	--	100	--	nC	
Q_{gs}	Gate Source Charge		--	25	--		
Q_{gd}	Gate Drain Charge		--	30	--		
$t_{D(on)}$	Turn-On Delay Time	$V_{GS} = 10\text{V}, V_{DS} = 40\text{V}, I_D = 20\text{A}, R_G = 2.5\Omega$	--	24	--	ns	
t_r	Turn-On Rise Time		--	19	--		
$T_{D(off)}$	Turn-Off Delay Time		--	70	--		
t_f	Turn-Off Fall Time		--	30	--		
t_{rr}	Body Diode Reverse Recovery Time	$I_F = 20\text{A}, di/dt = 100\text{A}/\mu\text{s}$	--	37	--	ns	
Q_{rr}	Body Diode Reverse Recovery Charge		--	58	--	nC	

A. Single pulse width limited by maximum junction temperature.

B. The maximum current rating is package limited.

C. The power dissipation P_D is based on $T_{J(MAX)} = 175^\circ\text{C}$, using junction-to-case thermal resistance, and is more useful in setting the upper dissipation limit for cases where additional heatsinking is used.



TYPICAL ELECTRICAL AND THERMAL CHARACTERISTICS

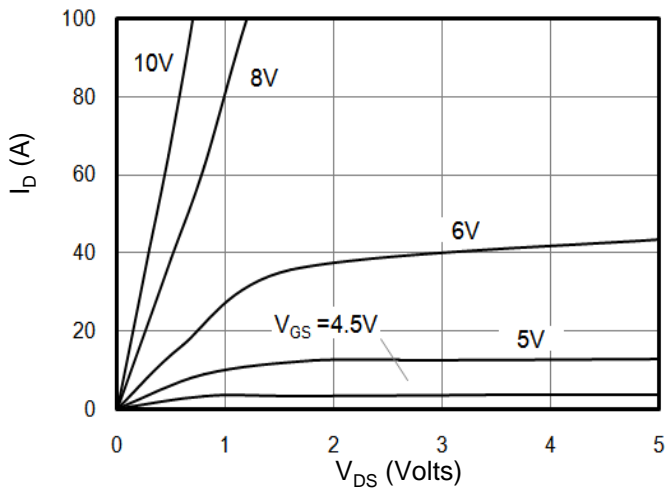


Figure 1: On-Region Characteristics

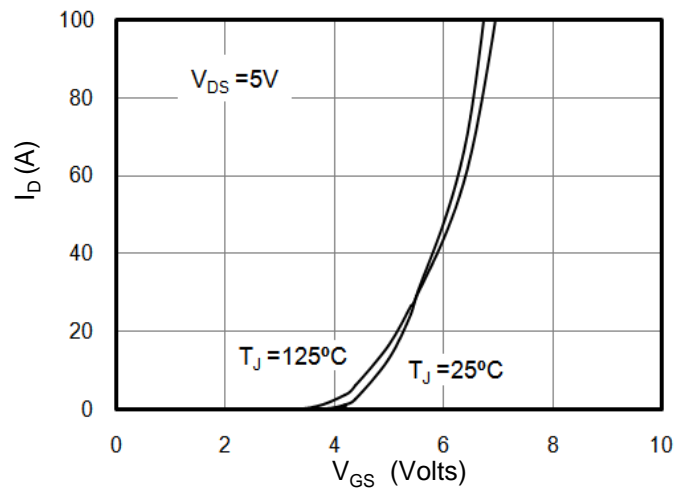


Figure 2: Transfer Characteristics

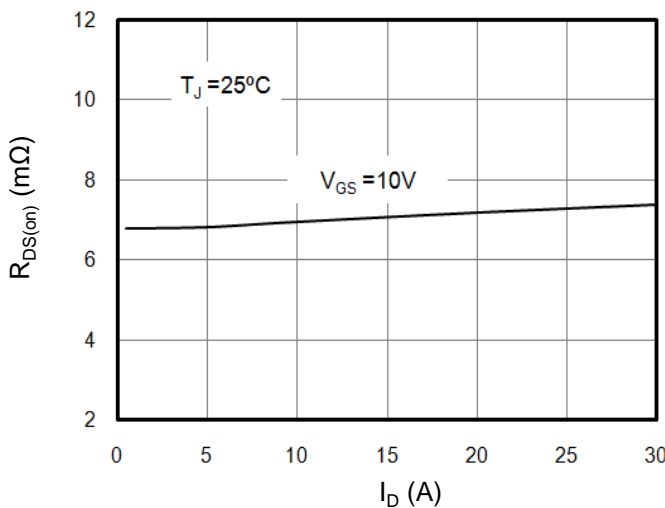


Figure 3: On-Resistance vs. Drain Current

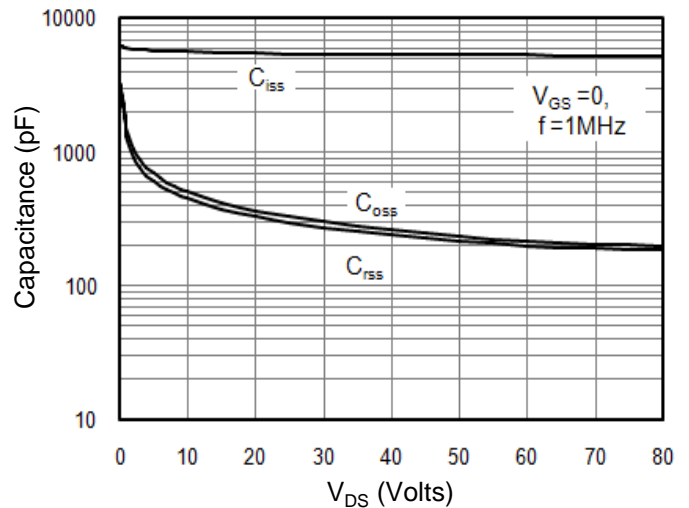


Figure 4: Capacitance Characteristics

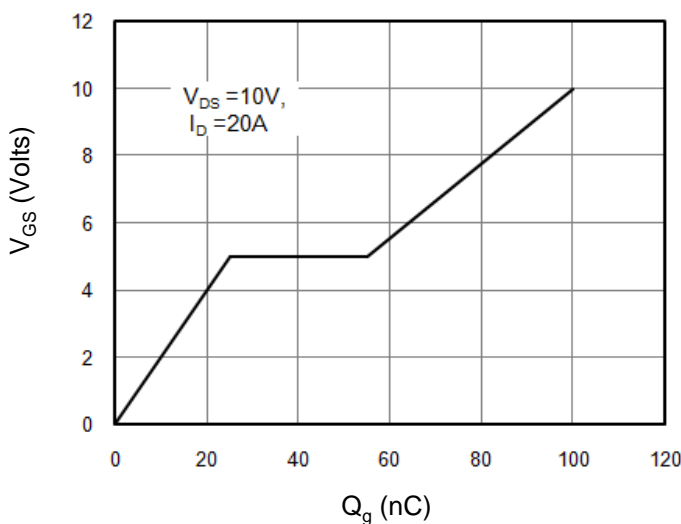


Figure 5: Gate Charge Characteristics

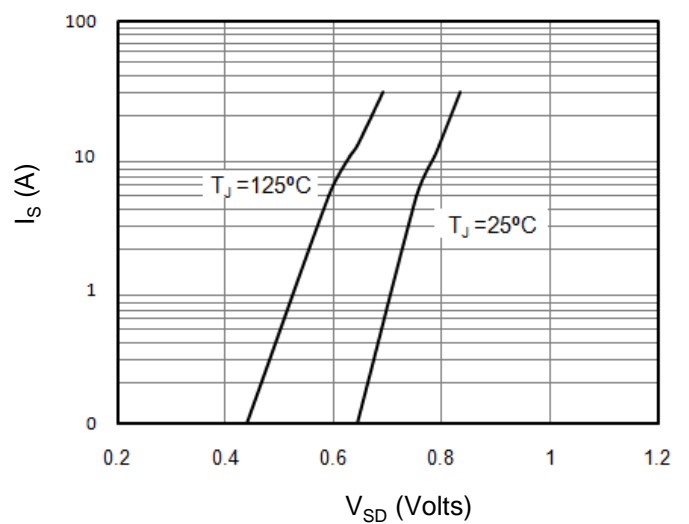


Figure 6: Body Diode Forward Voltage



TYPICAL ELECTRICAL AND THERMAL CHARACTERISTICS

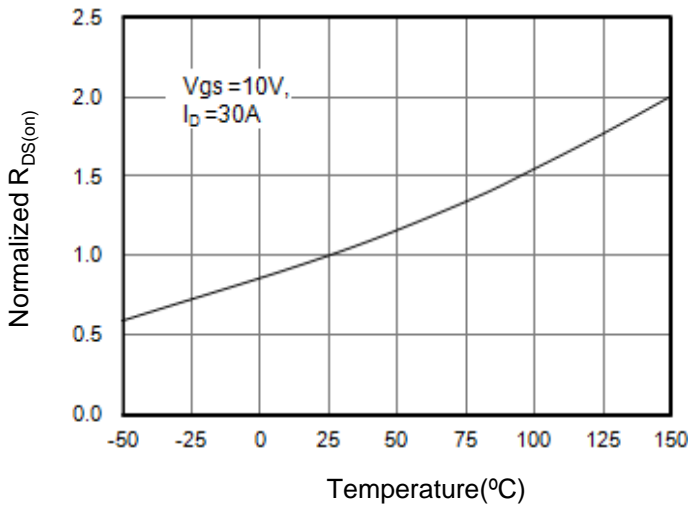


Figure 7: On-Resistance vs. Junction Temperature

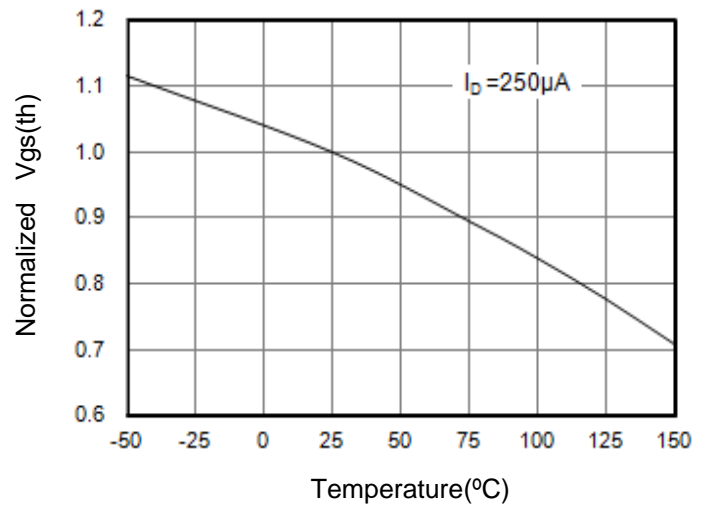


Figure 8: $V_{GS(th)}$ vs. Junction Temperature

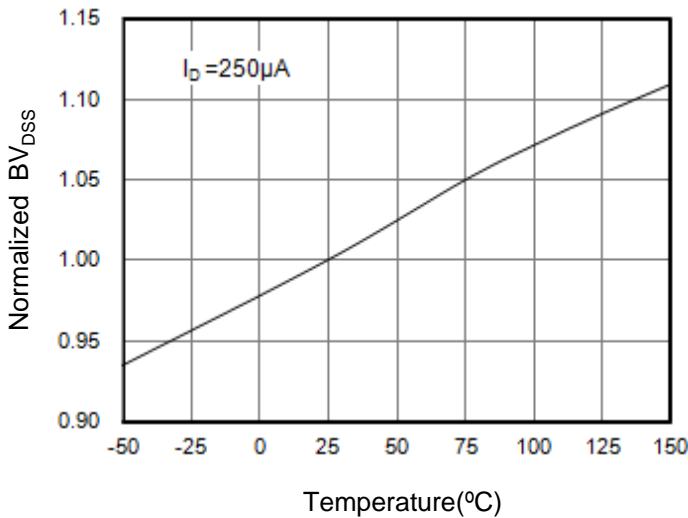


Figure 9: BV_{DSS} vs. Junction Temperature

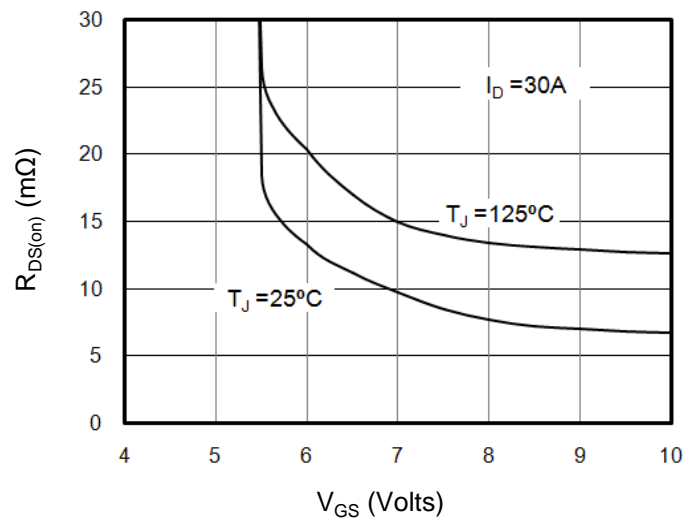


Figure 10: On-Resistance vs. Gate-Source Voltage

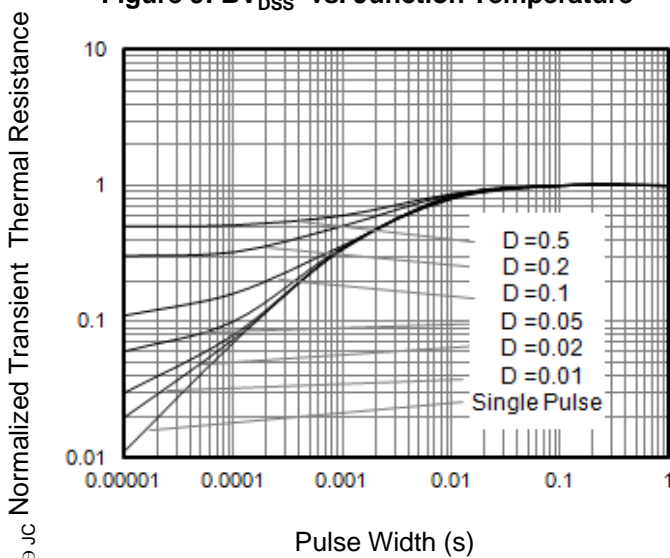


Figure 11: Normalized Transient Thermal Resistance

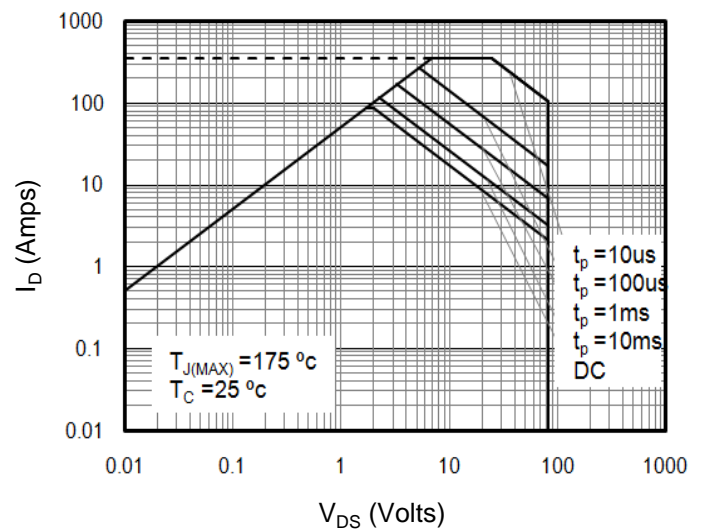


Figure 12: Safe Operating Area



Figure A: Gate Charge Test Circuit and Waveforms

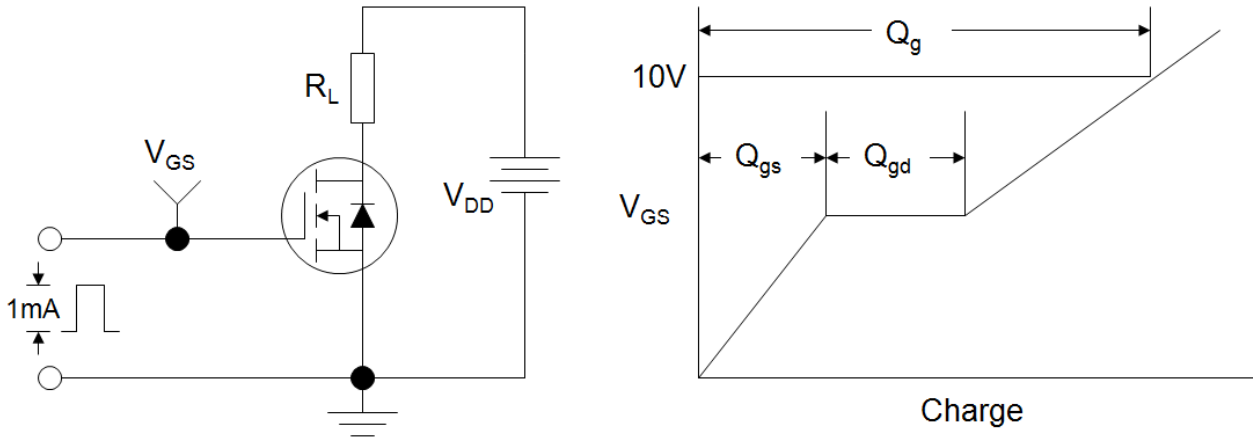


Figure B: Resistive Switching Test Circuit and Waveforms

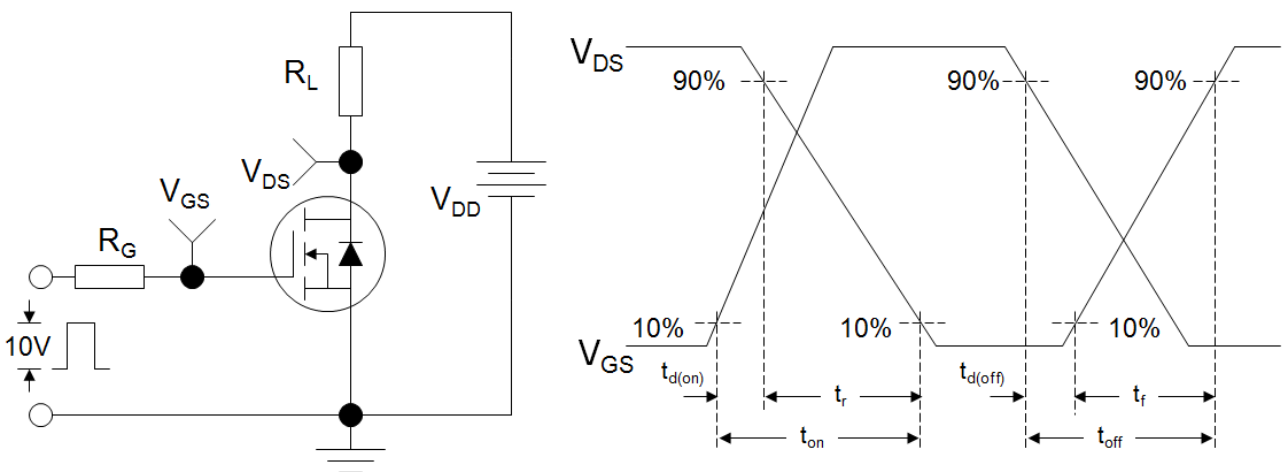
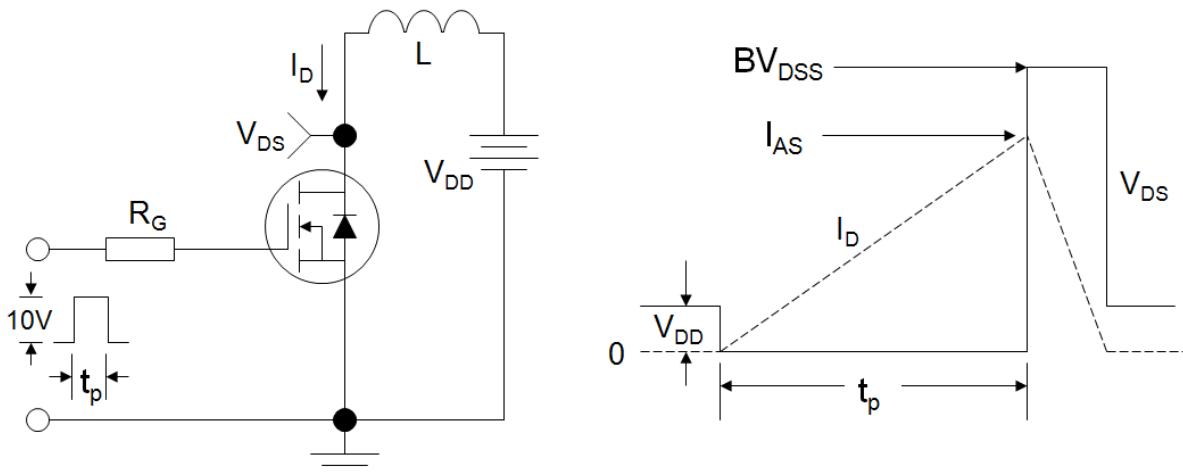
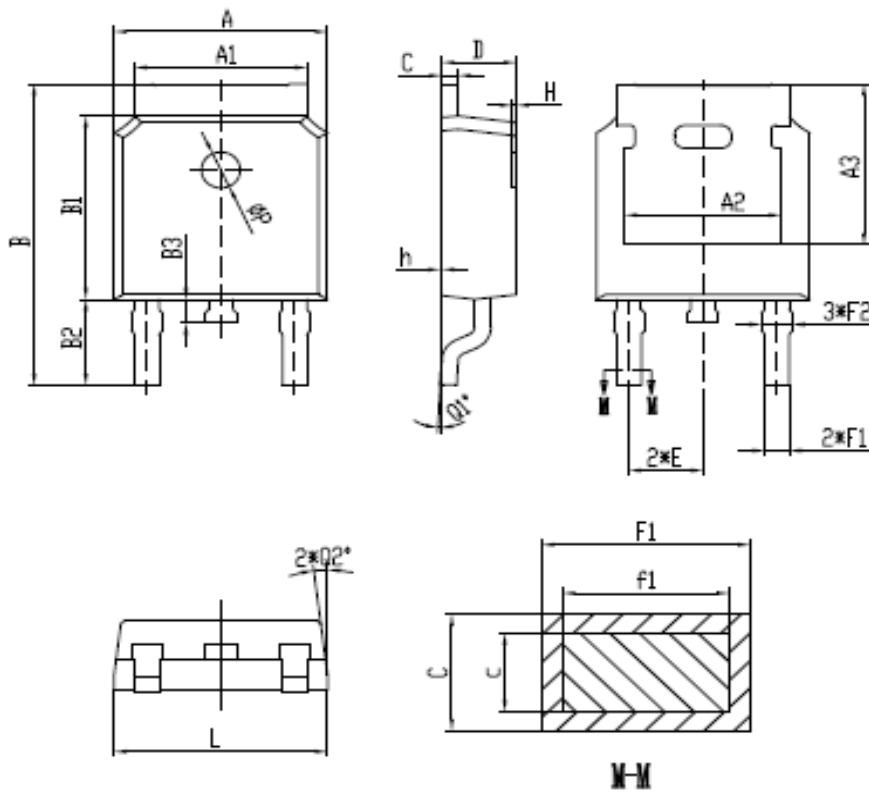


Figure C: Unclamped Inductive Switching (UIS) Test Circuit and Waveforms





TO-252(T)



SYMBOL	MIN	NOM	MAX
A	6.50	6.60	6.70
A1	5.16	5.31	5.46
A2	4.83 REF		
A3	5.30 REF		
B	9.77	9.97	10.17
B1	6.00	6.10	6.20
B2	2.60	2.80	3.00
B3	0.70	0.80	0.90
C	0.41	—	0.61
c	0.40	0.50	0.60
D	2.20	2.30	2.40
E	2.186	2.286	2.386
F1	0.67	—	0.87
f1	0.66	0.76	0.86
F2	0.76	0.86	0.96
H	0.00	—	0.30
h	0.00	—	0.20
L	6.50	6.60	6.70
$\varnothing P$	1.10	1.20	1.30
$Q1^*$	0°	—	8°
$Q2^*$	6°	7°	8°



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